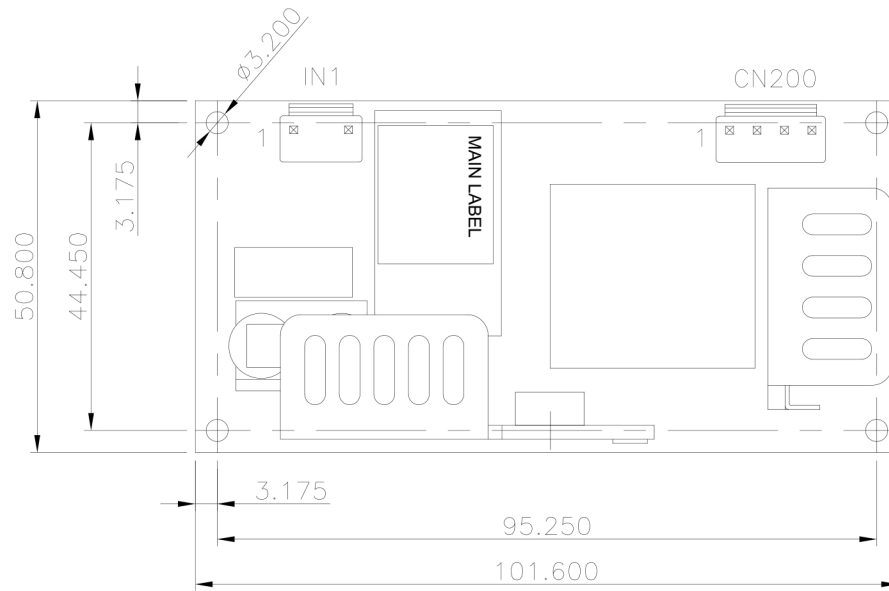




全漢企業股份有限公司
FSP TECHNOLOGY INC.



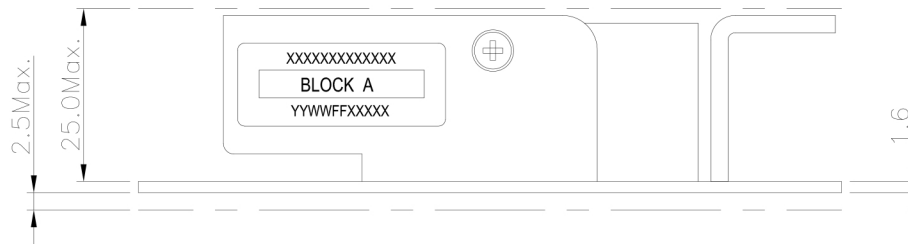
Pin assignment of IN1

Pin No.	Function	Wafer
1	N	JST B2P3-VH or EQUIVALENT
2		
3	L	

Pin assignment of CN200

Pin No.	Function	Wafer
1	+12V	JST B4P-VH or EQUIVALENT
2	+12V	
3	GND	
4	GND	

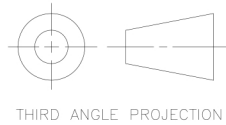
備註：產地標籤依業務指示加貼。



SCALE:
UNIT: mm

P/N.: 90C0655806

DIMENSIONAL TOLERANCES		HOLES : ±0.2		ANGLES : ±0.5°		MODEL NO. : FSP065-P24-A12,(F660001)	TITLE: ASSY	SHEET: 1 OF 1	REV: 01
(V)	()	()	()	()	()	R&D	P E	DRAWN	DATE
<30 :±0.25	DECIMALS	UP~100 :±0.2	250~300:±0.4	UP~100 :±1.0					FEB.14.2019
>30~100 :±0.35	X :±0.3	100~150:±0.25	300~350:±0.45	100~600 :±1.5					
>100~300 :±0.5	X.X :±0.2	150~200:±0.3	350~OVER:±0.5	600~900 :±2.4					
ABOVE 300 :±0.6	X.XX:±0.1	200~250:±0.35		900~OVER :±3.1					
						INTERIOR COUNTERSIGN:			



Z:\autocad\MODEL-NAME\60W\FSP065-P24-A12(F660001)\90C0655806.dwg